

20699vc2

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

R. HORI, et al.

Serial No.:

07/869,851

Filed:

April 16, 1992

For:

SEMICONDUCTOR INTEGRATED CIRCUIT WITH VOLTAGE LIMITER HAVING DIFFERENT OUTPUT RANGES FORM NORMAL OPERATION AND PERFORMING

OF AGING TEST

Group:

Examiner:

2306)

RECEIVED

S. Baker

AUS 1.7 1995 GEOUP 2000

## INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR §1.97 & 1.98

Honorable Commissioner of Patents and Trademarks Washington, D.C. 20231 August 14, 1995

sir:

In the matter of the above-identified application, applicants are submitting herewith copies of Form PTO-892 from related applications listing various references made of record in the related applications. Copies of the references can be found in the files of the related applications and apparently have already been considered by the Examiner.

It is respectfully requested that this information disclosure statement be considered by the Examiner.

Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees,

to the deposit account of Antonelli, Terry, Stout & Kraus Deposit Account No. 01-2135 (Case No. 501.20699VC2) please credit any excess fees to such deposit account.

Respectfully submitted,

(703) 312-6600 CIB/hpg

Carl I. Brundidge

Registration No. 29,621 ANTONELLI, TERRY, STOUT & KRAUS